

## DAFTAR PUSTAKA

- Abdurrahman, M. H., Setiawan, I., & Handoko, S. (2018). Desain Dan Implementasi Synchronous Reference Frame-Phase Locked Loop (Srf-Pll) Untuk Tegangan Satu Fase Menggunakan Dspic30F4011. *Transient*, 7(1), 145. <https://doi.org/10.14710/transient.7.1.145-151>
- Aditya, A. W., Utomo, R. M., Baladewa, A., Canda, A., & Septiadi, R. (2019). *Implementasi Hardware in the Loop ( Hil ) Pada Praktikum Berbasis Arduino – Matlab / Simulink*. 5662(November), 23–29.
- Chander, A. H., & Kumar, L. (2018). Design of a Synchronous Reference Frame Controller for Single Phase Standalone Photovoltaic Inverter. *2017 14th IEEE India Council International Conference, INDICON 2017*. <https://doi.org/10.1109/INDICON.2017.8487990>
- de Carvalho, G. U., Denardin, G. W., Cardoso, R., & Grando, F. L. (2021). A PID SRF-PLL based algorithm for positive-sequence synchrophasor measurements. *International Transactions on Electrical Energy Systems*, 31(3). <https://doi.org/10.1002/2050-7038.12777>
- Fuchs, E., & Masoum, M. (2008). Power Quality in Power Systems and Electrical Machines. In *Power Quality in Power Systems and Electrical Machines*. <https://doi.org/10.1016/B978-0-12-369536-9.X5001-3>
- Gamit, B. R., & Vyas, S. R. (2018). Harmonic Elimination in Three Phase System By Means of a Shunt Active Filter. *International Research Journal of Engineering and Technology (IRJET)*, 5(4), 313–322.
- Ghorbani, M. J., & Mokhtari, H. (2015). Impact of harmonics on power quality and losses in power distribution systems. *International Journal of Electrical*

- and Computer Engineering*, 5(1), 166–174.  
<https://doi.org/10.11591/ijece.v5i1.pp166-174>
- İnci, M., Bayındır, K. Ç., & Tümay, M. (2016). Improved Synchronous Reference Frame based controller method for multifunctional compensation. *Electric Power Systems Research*, 141, 500–509.  
<https://doi.org/10.1016/j.epsr.2016.08.033>
- J. C. Das. (2015). *Power System Harmonics and Passive Filter Designs*.
- Karimi-Ghartema, M. (2014). *Enhanced Phase-Locked Loop Structures for Power and Energy Applications*.
- Khairunnisa, & Indrasary, Y. (2016). Simulasi Akuisisi Data Sinyal Audio. *Jurnal Simantec*, 5(2), 76–84.
- Koerniawan, T., & Hasanah, A. W. (2019). Kajian Harmonisa Pada Pemakaian Tenaga Listrik Gedung STT-PLN Jakarta. *Kilat*, 8(2), 180–189.  
<https://doi.org/10.33322/kilat.v8i2.547>
- Kumar, C. S. S., & Gopalakrishnan, V. (2013). Investigation of Harmonic Detection Techniques for Shunt Active Power Filter. *IOSR Journal of Electronics & Communication Engineering (IOSR-JECE)*, 1, 68–73.
- Manik, P. T., Tejal Ramesh, P., & Patil Sunil, K. (2019). Power Factor Improvement of Nonlinear Load Using Boost Converter with Average Current Control. *2019 IEEE 5th International Conference for Convergence in Technology, I2CT 2019*, 1–4.  
<https://doi.org/10.1109/I2CT45611.2019.9033621>
- Naderipour, A., Zin, A. A. M., Habibuddin, M. H., Moradi, M., & Khavari, A. H. (2015). Control of compensation devices by synchronous reference frame

- and fourier control methods to improve the power quality in a microgrid.  
*2015 IEEE Student Conference on Research and Development, SCOReD 2015, July 2016*, 727–732. <https://doi.org/10.1109/SCORED.2015.7449434>
- Ng, K. M., Haziq Mohd Suhaimi, M. A., Ahmad, A., & Razak, N. A. (2019). Remote air quality monitoring system by using MyRIO-LabVIEW. *2018 9th IEEE Control and System Graduate Research Colloquium, ICSGRC 2018 - Proceeding, August*, 105–109.  
<https://doi.org/10.1109/ICSGRC.2018.8657501>
- Quraan, M. (2020). Error compensation algorithm for SRF-PLL in three-phase grid-connected converters. *IEEE Access*, 8, 182338–182346.  
<https://doi.org/10.1109/ACCESS.2020.3028834>
- Rafiq, A. A., Yusuf, M., & Pujono. (2018). Implementation of Digital Image Processing Using NI myRIO and Arduino Mega 2560 as Controller on Rover Bogie Robot. *Proceedings - 2018 International Conference on Applied Science and Technology, ICAST 2018*, 210–215.  
<https://doi.org/10.1109/iCAST1.2018.8751506>
- Rasheduzzaman, M., Khorbotly, S., & Kimball, J. W. (2016). A modified SRF-PLL for phase and frequency measurement of single-phase systems. *ECCE 2016 - IEEE Energy Conversion Congress and Exposition, Proceedings*.  
<https://doi.org/10.1109/ECCE.2016.7854707>
- Sanjan, P. S., Yamini, N. G., & Gowtham, N. (2020). Performance Comparison of Single-Phase SAPF Using PQ Theory and SRF Theory. *2020 International Conference for Emerging Technology, INCET 2020*, 1–6.  
<https://doi.org/10.1109/INCET49848.2020.9154126>

- Sarma, M. S., & Vedam, R. S. (2008). *Power Quality VAR Compensation in Power Systems*.
- Siregar, M. F., Hidayat, J., Bahri, S., & Pendahuluan, I. (2018). Perbandingan Nilai Distorsi Harmonisa pada Tiga Buah Laptop yang Berbeda. *Journal of Electrical Technology*, 1099, 86–89.
- Sonam, K., Nikhil, P., Sudeep, B., & Atul, G. (2017). Implementation of single-phase modified SRF-PLL using model based development approach. *2017 North American Power Symposium, NAPS 2017*, 1.  
<https://doi.org/10.1109/NAPS.2017.8107296>
- Vinnakoti, S., & Kota, V. R. (2017). *SRF and Real Power Theory based Control of a Nine Switch Converter based UPQC*.
- Vishal, K. (2018). Controller Applied To Three Phase Grid Interfaced Pv-System for Power Quality. *2018 IEEE Innovative Smart Grid Technologies - Asia (ISGT Asia)*, 740–745.

## LAMPIRAN

### Lampiran 1. Pengujian Sistem Membaca Amplitudo

Amplitudo Sumber (A)	Amplitudo Terbaca (A)
1	1,00008
1	1,00009
1	1,00009
1	1,00004
1	1,00007
1	1,0001
1	1,00005
1	1,00008
1	1,0001
1	1,00007